

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,102,415 B1
APPLICATION NO. : 10/810120
DATED : September 5, 2006
INVENTOR(S) : Vladislav Potanin et al.

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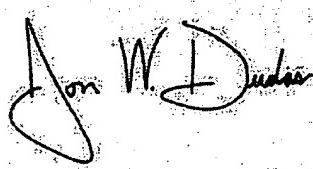
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 6, Line 15: "Transistors MIA" should read --Transistors M1A --

Column 6, Line 28: "transistor MIA" should read -- transistor M1A--

Signed and Sealed this

Sixth Day of March, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office